

SAW RF filter

Short range devices

Series/type: B3563

Ordering code: B39861B3563U410

Date: October 21, 2013

Version: 2.0

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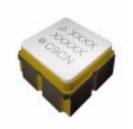
SAW RF filter 864.00 MHz

Data sheet

SMD

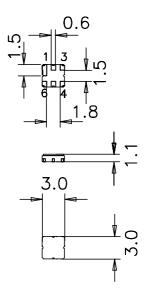
Application

- Low-loss RF filter for wireless audio application
- Low amplitude ripple
- High image frequency suppression
- No matching network required for operation at 50 Ω



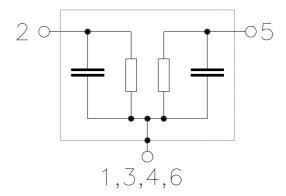
Features

- Package size 3.0 x 3.0 x 1.1 mm³
- Package code DCC6C
- RoHS compatible
- Approximate weight 0.037 g
- Package for Surface Mount Technology (SMT)
- Ni, gold-plated terminals
- Lead free soldering compatible with J STD20C
- Electrostactic Sensitive Device (ESD)



Pin configuration

- 2 Input
- 5 Output
- 1, 3, 4, 6 To be grounded





B3563

SAW RF filter 864.00 MHz

Data sheet

Characteristics

0 °C to +40 °C Temperature range for specification:

 $T_A = Z_S = Z_L = Z_L$ Terminating source impedance: 50 Ω Terminating load impedance: 50 Ω

		min.	typ.	max.	
			@ 25 °C		
Center frequency	f _C	_	864.00	_	MHz
Maximum insertion attenuation	α_{max}				
862.50 865.50 MHz		_	4.0	6.0	dB
Amplitude ripple (p-p)	$\Delta \alpha$				
862.50 865.50 MHz		_	1.5	2.5	dB
Relative attenuation (relative to α_{max})	α_{rel}				
841.60 843.60 MHz		40.0	44.0	_	dB
852.30 853.30 MHz		30.0	38.0	_	dB
879.00 915.00 MHz		30.0	38.0	<u> </u>	dB
Temperature coefficient of frequency	TC_{f}	_	-30	_	ppm/K

Maximum ratings

Operable temperature range	T _A	-45/+125	°C	
Storage temperature range	T_{stg}	-45/+125	°C	
DC voltage	V_{DC}	6	V	
Input power max.	P_{IN}	0	dBm	source impedance 50 Ω

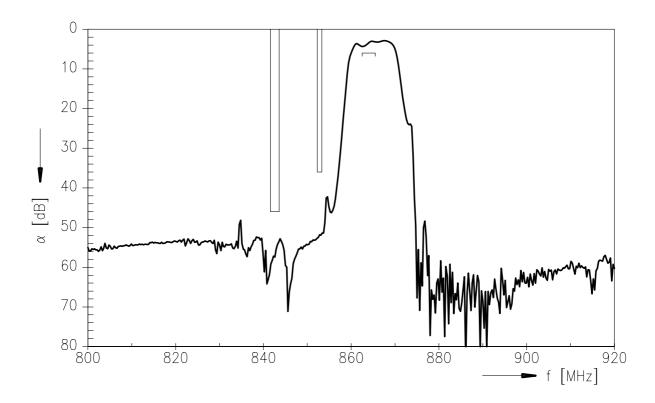


SAW RF filter 864.00 MHz

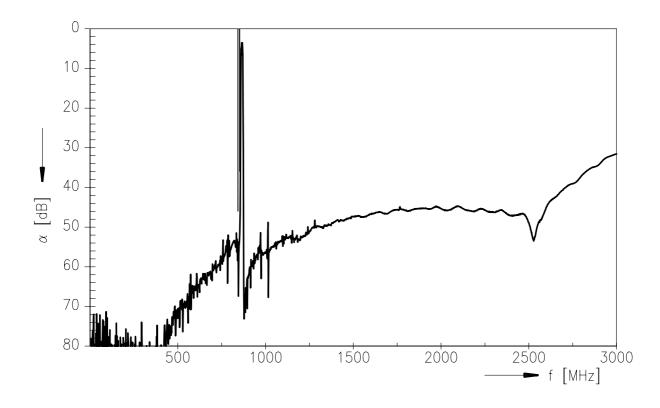
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Transfer function (passband)



Transfer function (wideband)





SAW RF filter 864.00 MHz

Data sheet



ESD protection of SAW filters

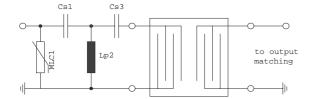
SAW filters are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wideband filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.



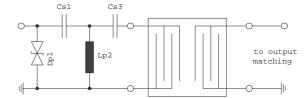


Fig. 1 MLC varistor plus ESD matching

Fig. 2 Suppressor diode plus ESD matching

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

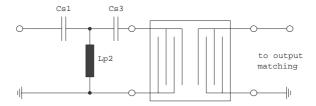


Fig. 3 3rd order high-pass structure for basic ESD protection

In all three figures the shunt inductor Lp2 could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available pcb space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements

For further information, please refer to EPCOS Application report:

"ESD protection for SAW filters".

This report can be found under www.epcos.com/rke.Click on "Applications Notes".



SAW RF filter 864.00 MHz

Data sheet



References

Туре	B3563	
Ordering code	B39861B3563U410	
Marking and package	C61157-A7-A67	
Packaging	F61074-V8168-Z000	
Date codes	L_1126	
S-parameters	B3563_NB.s2p, B3563_WB.s2p See file header for port/pin assignment table.	
Soldering profile	S_6001	
RoHS compatible	RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.	
Matching coils	See Inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm	

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